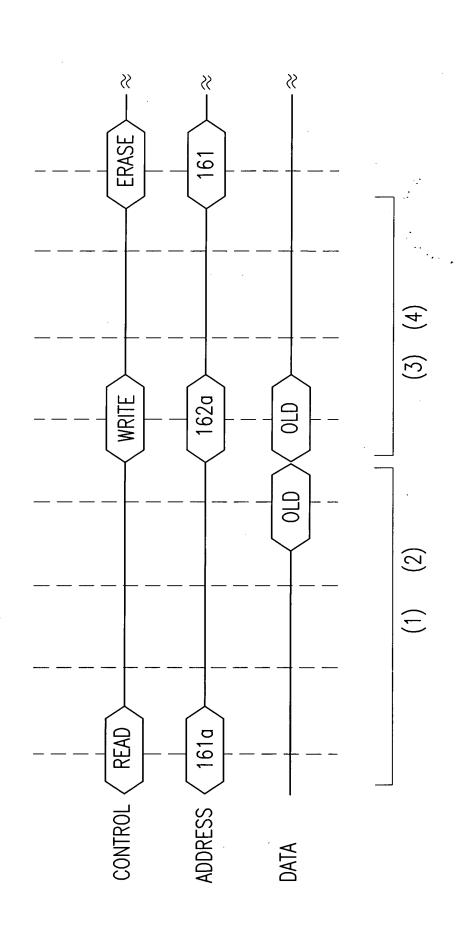


161a 162a , 161 BLOCK M BLOCK N 100 4  $\Xi$ FLASH MEMORY DEVICE BUFFER 150 (3) *FIG. 2A* PRIOR ART BUS 300 (2) CONTROLLER BUFFER CONTROLLER 201 200

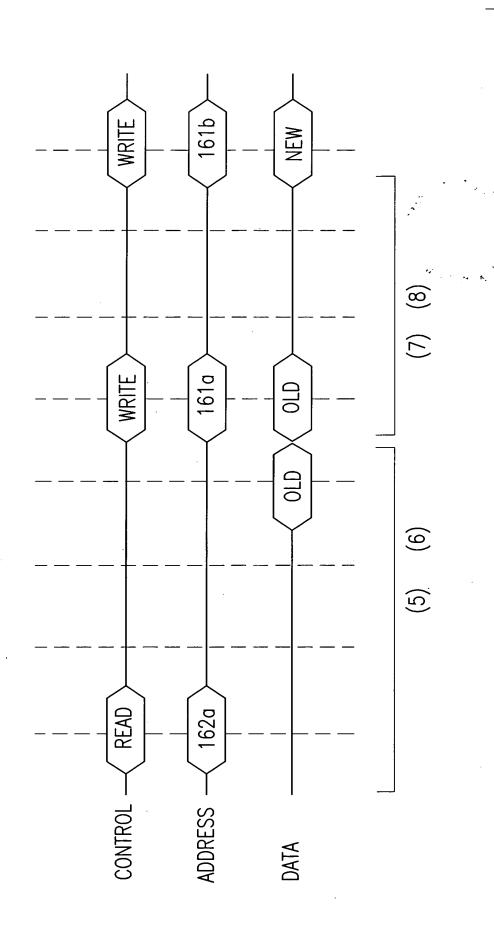
161b 162a 161a 162 , 161 BLOCK M BLOCK N 100 (2) FLASH MEMORY DEVICE BUFFER (8) 150 *FIG. 2B* PRIOR ART (7) BUS 300 (9) CONTROLLER BUFFER CONTROLLER 201 200

FIG. 3A PRIOR ART



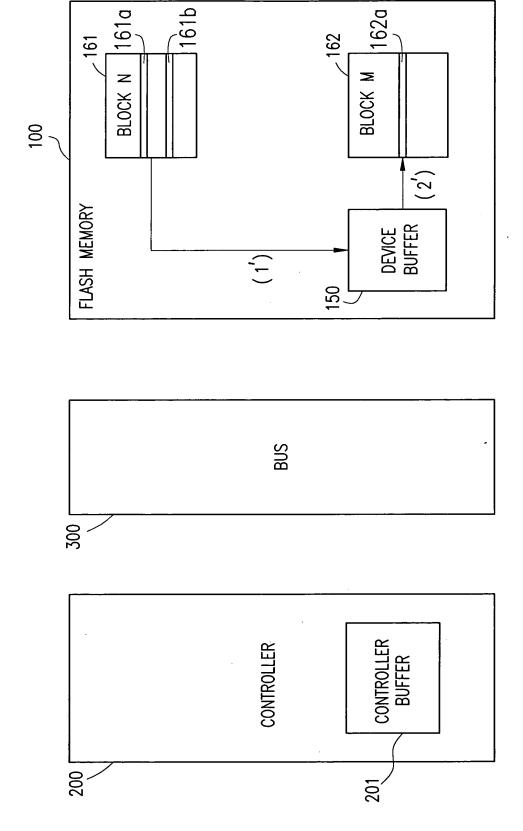
+

FIG. 3B PRIOR ART



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FIG. 4A



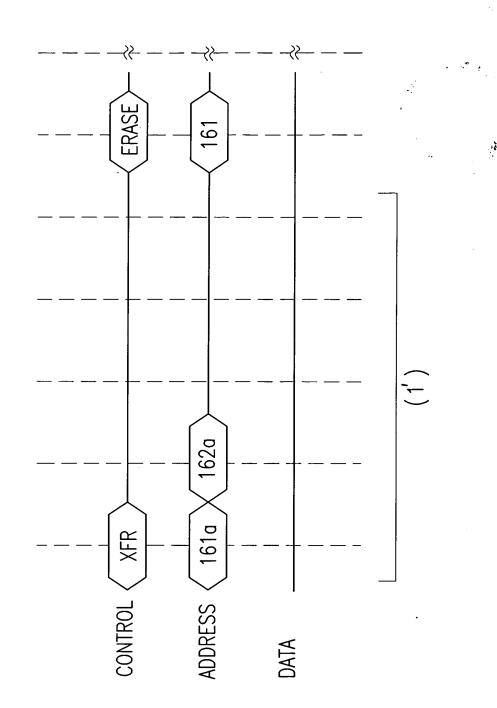
161b

162a

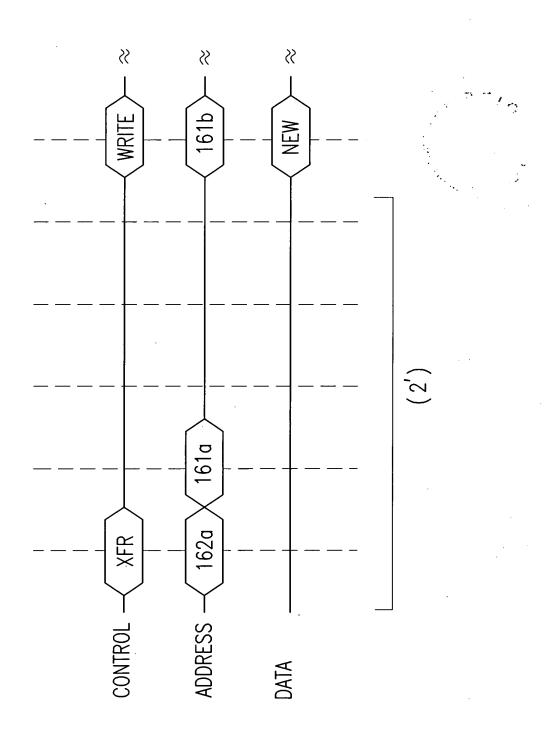
162

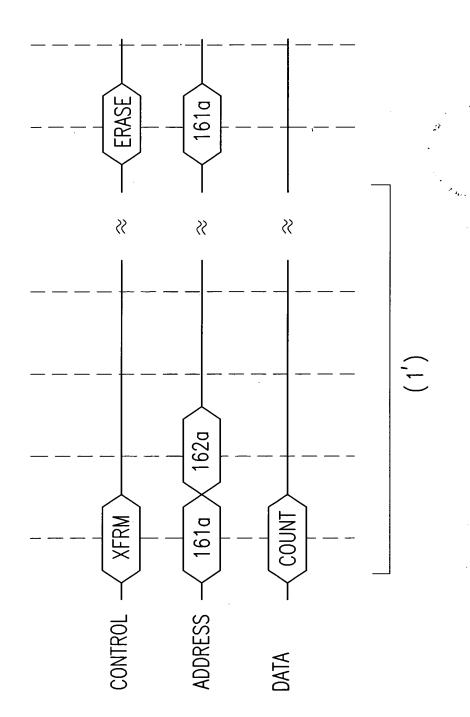
161a

161



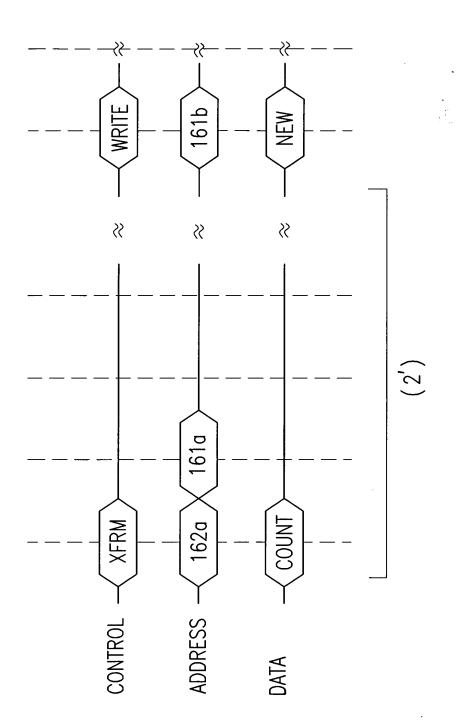
+



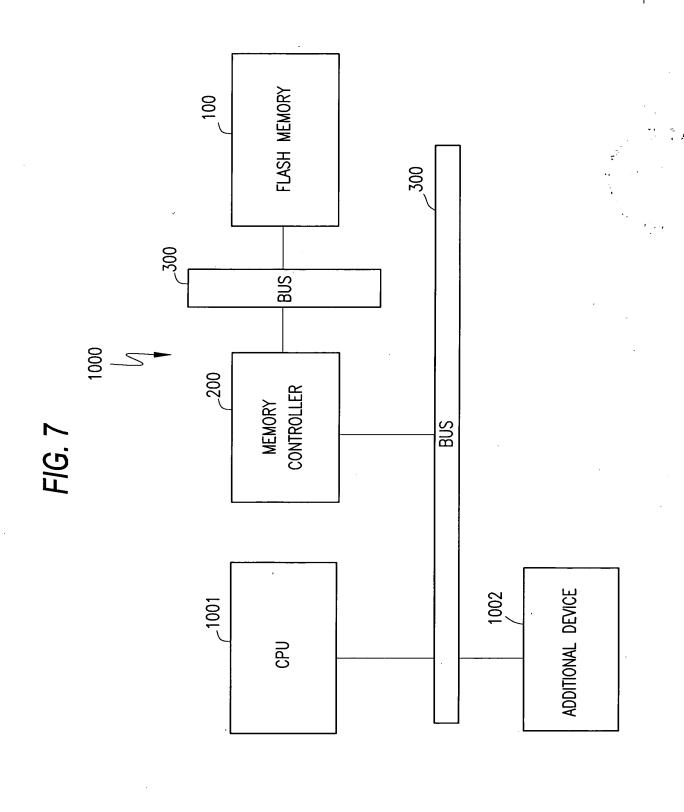


+

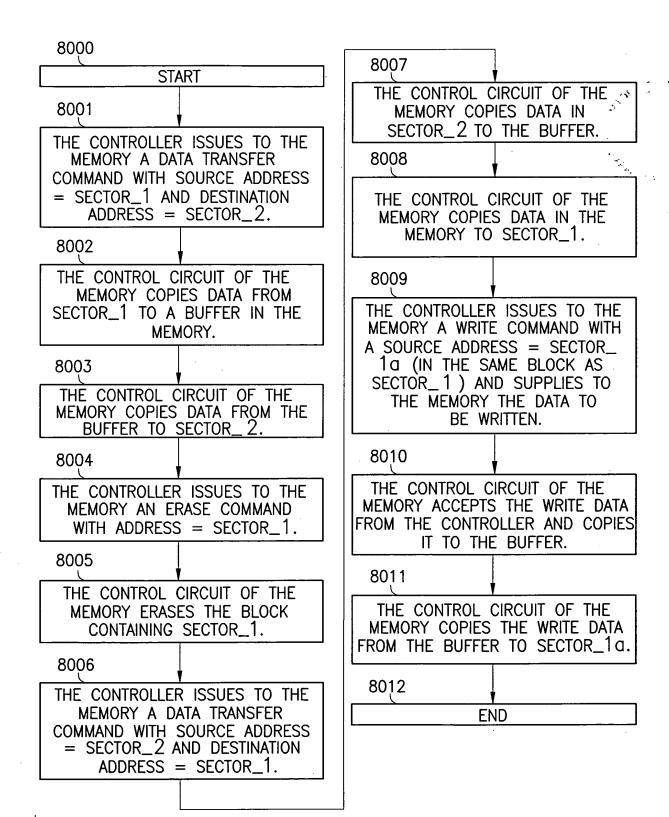
FIG. 6B



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## FIG. 8A



## FIG. 8B

